



App. No. 10/056,373

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Ernst Bretschneider, et al	:	
Appl. No.	:	10/056,373	:	Grp./Art Unit : 2859
Filed	:	17 January 2002	:	Examiner : T. Reis
Title	:	Circuit Arrangement And Method	:	
		Of Protecting At Least A Chip	:	
		Arrangement From Manipulation	:	
		And/Or Abuse	:	
Docket No.	:	DE010005	:	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE and AMENDMENT

Sir:

This is in response to the Office Action of 22 October 2004.

Presented below, and beginning on separate sheets, are various sections, including, a Claim Amendments section, and a Remarks section.